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Phone		Date:	JAN 3	0 2006
Faxc	571.273.8300	Pages:	29, including this c	over sheet
То:	USPTO	From:	Michelle Craig, Reg	J. No. 52,776

Please find attached for filing in connection with application no. 10/613,372, entitled ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM, the following documents:

- REV/POA
- Statement Under 37 CFR 3.73(b)
- Copy of Assignment of Patent Rights executed 10/31/2005 (10 pages)
- Copy of Assignment of Patent Rights executed 12/23/2005 (16 pages)

### CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is U.S. Patent and Trademark Office on:	beli	ng transmitted by facsimile to t
JAN 8	0	2006
Date of Tran	smi	ission
Julianne Flyn		
Name of Person Transmi	ttiny	g-Correspondence
- John J		
Signal	ure	

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hereby revoke all previous powers of attorney g 7 CFR 3.73(b).	iven in the application	identified in the attached	statement under	
hereby appoint:	<u> </u>	<del></del>	•	
Practitioners essociated WIU the Oustomer Number:	00043831			
OR  Practitioner(s) nemed below (if more than ten patent pr	acillioners are to be named	, then a customer number in ust	be used):	
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signes Name and Address;				,
Zarbaña Digital Fund LLC				
2711 Centerville Road, Suite 400	• •			•
Wilmington, DE 19808	·			
copy of this form, together with a statement under ed in each application in which this form is used. e practitioners appointed in this form if the appoin ad must identify the application in which this Powe	The statement under 3: ted practitioner is auth	7 CFR 3,73(b) may be comp orized to act on behalf of t	pleted by one of	
SIGNATUR The individual whose signature and title is a	RE of Assignee of Record supplied below is sultorize	d to not on behalf of the assigne	e	
matura 1		Date 115/04		<del>-                                    </del>
me Bryan Burpee, Authorized Person fo	r Zerbaña Digital Fun	nd LLC Telephone	ć- <u></u>	•
e .				
s collection of Information is required by 37 OFR 1.81, 1.82 and 1.83, the USPTO to propess) an application, Contidentially is governed by compile, including gelicating, preparing, and submitting the complete mounts on the amount of time you require to complete this form and C. Palent and Triedement Otilica, U.S. Department of Commance, P	y 85 U.S.C. 122 and 37 CFR 1; d application form to the USPTO <u>for suppositions for matering th</u>	11 and 1,14. This collection is eathn D. Tima will vary depending upon th is burden, should be sent to the Ohl	eled to take 3 minutes e individual case. Any laf-information-Offices:	·

PTO/SB/96 (08-03)
Approved for use through 07/31/2005, OMB 0651-0031
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STATEMENT L	UNDER 37 CFR 3.73(b)
Applicant/Patent Owner: Zarbaña Digital Fund LLC	·
Application No./Patent No.: 10/813.372 Filed	1/Issue Date: 7/3/2003
Entitled: ADAPTIVE PREDISTORTION FOR A TRANSMIT S	
Zerbafia Digital Fund LLC a	Limited Liability Company
(Name of Assignee)	Type of Assignee, e.g., corporation, parinership, university, government agency, etc.)
states that it is: 1. ☑ the assignee of the entire right, title, and interest; or	·
'2.   an assignee of less than the entire right, title and interest the extent (by percentage) of its ownership interest in the patent application/patent identified above by virtue or	ls ————— % of either:
A. [ ] An assignment from the inventor(s) of the patent applied in the United States Patent and Trademark Office at attached.	plication/patent identified above. The assignment was recorded Reel Frame or for which a copy thereof is
OR	
below:	plication/patent identified above, to the current assignee as shown
The document was recorded in the United St	To: Icefyre Semiconductor Corporation tates Patent and Trademark Office at , or for which a copy thereof is attached.
From: Icefyre Semiconductor Corporation     The document was recorded in the United St	
The document was recorded in the United St Reel, Frame	or for which a copy interest is attached.
3. From: Lestyre Semiconductor, Inc.	To: Zerbaña Digital Fund LLC
The document was recorded in the United St	tates Patent and Trademark Unice at or for which a copy thereof is attached.
[ ] Additional documents in the chain of title are	b listed on a supplemental sheet.
[v] Copies of assignments or other documents in the chal [NOTE: A separate copy (i.e., the original assignment must be submitted to Assignment Division in accorda recorded in the records of the USPTO. See MPEP 30	t document or a true copy of the original document, ince with 37 CFR Part 3, if the assignment is to be
The undersigned (whose title is supplied below) is authority 1-25-64	ized to act on behalf of the assignee.  Michelle Cratg, Reg. No. 52,776
Date	Michelle Cratg, Reg. No. 52,778  Typed of printed name
<u>503-439-650</u>	Michila
Telephone number	Signature
	Attorney at Law Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiatily is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the Individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patient and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patenta, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

### PATENT ASSIGNMENT

IcePyre Semiconductor Corporation, c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

(hereinafter "Assignor")

IceFyre Semiconductor, Inc., c/o Fraser Milner Casgrain LLP, 99 Bank Street, Suite 1420, Ottawa, Ontario, K1P 1H4

### (hereinafter "Assignee")

WHEREAS the Assignor and Assignee executed the Intellectual Property Transfer Agreement dated October 3/,2005, which provides for the purchase by Assignee of certain patent rights of Assignor; and

WHEREAS the Assignor, in consideration of CAD one dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, do hereby sell, assign and transfer to the Assignee and its successors and assigns:

- (i) Assignor's entire right, title and interest in Canada, the United States and throughout the world in and out to the patents and patent applications listed in Schedule A attached hereto, including any and all inventions described therein, and in any and all continuations-in-part, continuations, divisions, substitutes, reissues, re-examinations, or extensions thereof, and all other applications for patent relating thereto which have been filed, or hereafter shall be filed, in Canada, the United States or in any other jurisdiction and further including all rights under treaties to file and prosecute patent applications corresponding to the preceding patents and patent applications (the "Patents");
- (ii) all of Assignor's corresponding right, title and interest in and to any patents which may issue therefore, the same to be held and enjoyed by Assignee to the full end of the term for which the said patent is granted and maintained, as fully and entirely as the same could have been held and enjoyed by Assignor, and;
- (iii) the right to take action and recover in respect of any infringement of the Patents that took place prior to the date of this Assignment.

The Assignor hereby authorizes the issuance of any and all registrations for the Patents to the Assignee, its successors, assigns or legal representatives.

The Assignor hereby irrevocably designates and appoints the Assignee and its duly authorized officers and agents as the Assignor's agent and attorney in fact, to act for and on the Assignor's behalf and stead, to do all such lawful acts and things and to execute such further lawful assignments, documents, assurances, applications and other instruments as reasonably may be required by the Assignee, its successors, assigns or legal representatives, to obtain any and all

2

registrations for the Patents and to vest the same in the Assignee, its successors, assigns or legal representatives.

The Assignor hereby agrees to execute and sign all documents required to effect a recordation of the assignment of the Patents and registrations thereof before the proper office or agency.

The remainder of this page is intentionally left blank.

EXECUTED at: To RONGO this 3   day of October, 2005
IceFyre Semiconductor Corporation
Name: / Jim-Laird Title: / Director
Tibe.
STATEMENT OF WITNESS
I, whose full post office address is was
personally present and did see, who is known to me,
execute the above assignment.
Name: 3/A+ 2005

COUNTERPART SIGNATURE PAGE TO PATENT ASSIGNMENT

403504\_1.DOC

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

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	Comments	penssi		Request for Examination due December 3, 2005.			lssued	Abandoned		Abandoned		Abandoned, but still revivable through 1/06.			Abandoned, but still revivable through 1/05.
	Foreign Patents	Yes. See Below.						Yes. See Below.							
	Fees Current and Pald	Yes		Yes	Yes	Yes	√es	0 <u>N</u>		Yes	Yes	Yes	, Xes	Yes	Yes
	Published / Unpublished	Published		Published	TBD	IBU	Published	Published		Published	Published	Published	Published	TBD	TBD
	Number	6603352		2003-550250	7008505/2004	2824126.6	980,7586	10/068,120		2455111	2818192.1	2748525.9	2003-518082	7001206/2004	20040269
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	(0)	JE IER OWER		Japanese Nationalization	nalization	ionalization	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER COMBINING (CIP)	SELECTABLE INVERSION/VARIABLE GAIN COMBINER FOR DIVERSITY RECEPTION IN RF TRANSCEIVERS	ation	Canadian Nationalization	Chinese Nationalization	European Nationalization	Japanese Nationalization	Korean Nationalization	tíon .
	Title	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POWER	PCT Application	Japanese Ne	Korean Nationalization	Chinese Nationalization	SWITCHED-MODE POWER AMPLIFIER INTEGRALLY PERFORMING POW COMBINING (CIP)	SELECTABLE INVERSION/VARIA GAIN COMBINER F DIVERSITY RECEPTION IN RF TRANSCEIVERS	PCT Application	Canadian	Chinese Na	European h	Japanese	Korean Na	Norwegian Nationalization
	tem	ICE-001	CE-001PC		ICE-001KR	ICE-001CN	ICE-001CP	ICE-002	ICE-002PC	ICE-002CA	ICE-002CN	ICE-002EP	ICE-002JP	ICE-002KR	ICE-002NO

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

Title							100000		
PSUEDO-NOISE	tem	Title	S	fus		1 2	and Paid	Foreign Patents	Comments
DOWN CONVERTERS         Pending         PCT/CA02/01498         Published         Yes           OFT Application         Pending         10/154,282         Published         Yes           PCT Application         Pending         10/154,282         Published         Yes           PCT Application         Pending         PCT/CA02/01497         Published         Yes           PCT Application         Pending         PCT/CA02/01499         Published         Yes           PROCESING         METHOD FOR         Pending         TBD         Not Published         Yes           SPC RECOUNTAY AND         PROCESSING         Pending         TBD         Not Published         Yes           SPC PCT Application         Nationalization         Pending         TBD         Not Published         Yes           PCC Application         Abandoned         PCT/CA2004/0002         Published         Yes           PCC Application         Abandoned         TOOT/TS/2006 <td>ICE-003</td> <td>PSUEDO-NI CARRIER SUPPRESSI REJECTION</td> <td></td> <td></td> <td></td> <td></td> <td>sa,</td> <td></td> <td>Allowed, Issue Fee due about 1/20/03.</td>	ICE-003	PSUEDO-NI CARRIER SUPPRESSI REJECTION					sa,		Allowed, Issue Fee due about 1/20/03.
CONVERSION		DOWN CON	RTERS	1	1 8671/C007/1709		Yes		Abandoned
Charles   Char	2E-003PC	PC Applica	1	5					
Pending	CE-004	CONVERSIC CIRCUITRY		Pending	·	Published	Yes	PCT Application Pending.	
PCT   Application   Pending   PCT/CA02/01499   Published   No Offsets		KADIO - KA	JEIVER		1	Publiched	Yes		
Oscillator Frequency         Abandoned         10/155,107         Published         No           Offsets         PCT/CApplication         Pending         PCT/CA02/01499         Published         Yes           PHASOR         FRAGMENITATION         Pending         10/273,908         Published         Yes           PROCESSING         MODULATED SIGNALS         HAVING NON-         Pending         TBD         Not Published         Yes           PCT Application         Nationalization         Pending         TBD         Not Published         Yes           SYSTEMS AND         MODULES FOR USE         Pending         PCT/CA2004/0002         Published         Yes           DECODING         POET Application         Abandoned         PCT/CA2004/0002         Published         Yes           PRALLEI         PARALLEI         Pending         10/629,644         Published         Yes           PRALLEI         PRALLEI         Pending         TRD         Not Published         Yes           PRALLEI         Poending         10/629,644         Published         Yes           POTT Application         Nationalization         Pending         TRD           Rownoul Malloration         Pending         TRD         Yes	CE-004PC	PCT Applica		Penamg	1	ו ממוופוופת	2		
PCT Application	CE-005	Oscillator Fr		Abandoned	10/155,107	Published	No	PCT Application Pending.	Abandoned
PHASOR FRAGMENTATION CIRCUITRY AND METHOD FOR PROCESSING MODULATED SIGNALS PROTESPING MODULATED SIGNALS PROTESPING MODULATED SIGNALS POT Application Japanese Nationalization MODULES FOR USE WITH TRELLIS-BASED DECODING DECODING Pending TBD Not Published Yes POT Application Abandoned RCONOLL TIONAL Pending TOOT719/2005 Not Published Yes RCONOLL TIONAL RCODER RCONOLL TIONAL RCONOL	CF-005PC	PCT Applica		Pending		Published	Yes		
HAVING NON- PCT Application Japanese Nationalization Pending TBD Not Published Yes SYSTEMS AND MODULES FOR USE WITH TRELLIS-BASED DECODING PCT Application PRALLEI CONVOLUTIONAL PRACTICAL CONVOLUTIONAL PROTABBIGATION PENDING Nationalization Pending T001719/2005 Not Published Yes	ICE-006	PHASOR FRAGMEN CIRCUITRY METHOD F PROCESSI	ON O	Pending	10/273,908	Published	Yes	Yes. See Below.	
PCT Application   Nationalized   TBD   Not Published   Yes		HAVING N	NC						·
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SYSTEMS AND         Pending         10/377,859         Published         Yes           WITH TRELLIS-BASED         Pending         10/377,859         Published         Yes           DECODING         PCT/CAZ004/0002         Published         Yes           PCT Application         Pending         10/629,644         Published         Yes           PCT Application         Nationalization         Pending         7001719/2005         Not Published         Yes           Chicago Nationalization         Pending         7001719/2005         Not Published         Yes	ICE-006JP	Japanese	Vafionalization	Pending	ТВО	Not Published	Yes		
PCT Application         Abandoned         PCT/CA2004/0002 Published         Yes           PARALLEL         CONVOLUTIONAL         Pending         10/629,644         Published         Yes           ENCODER         Nationalized         Nationalized         Yes           Korean Nationalization         Pending         7001719/2005         Not Published         Yes           Chicasa Nationalization         Dending         7001719/2005         Not Published         Yes	ICE-007	SYSTEMS MODULES WITH TRE	AND FOR USE LLIS-BASED	Pending	10/377,859	Published	Yes		
PARALLEIL         Pending         10/629,644         Published         Yes           CONVOLUTIONAL         Pending         10/629,644         Published         Yes           ENCODER         Nationalization         Nationalization         Pending         7001719/2005         Not Published         Yes           Chicago Nationalization         Pending         TRD         Not Published         Yes	ICE-007PC	PCT Applic	ation	Abandoned	PCT/CAZ004/0002 82	- Published	. Yes		Abandoned
PCT Application Nationalized TO01719/2005 Not Published Chicago Nationalization Dending TRD Not Published	ICE-008	PARALLEI CONVOLL ENCODEF	TIONAL	Pending	10/629,644	Published	Yes	Yes. See Below.	
Korean Nationalization Pending 7001719/2005 Not Published	ICE-008PC	PCT Applic	tation	Nationalized					
Chinase Nationalitation Dending TBD TBD	ICE-008KR	Korean Na	tionalization	Pending	, 7001719/2005	Not Published	Yes		
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IceFyre Semiconductor Corp. Patent Related Informalion As of September 22, 2005

nts						peu		. pauc			Response to Examination Report Due December 15, 2005.			Abandoned, but still revivable through 1/06.	
Comments		-				Abandoned		Арапфолеф			Response to Examination Due Decemb 2005.			Aband revival 1/06.	
Foreign Patents			Yes. See Below.			Yes. See Below.									
rent	and Paid	Sal	Yes	Yes		No		Yes	Yes		Yes	Yes	Yes	Yes	Yes
		Not Published	Published	Published		Published		TBD	Published		Published	Published	TBD ·	TBD	Published
N. H.		2004-525088	10/629,640	PCT/CA03/01132	7	09/918,108		2,455,277	2818664.8		2748528.3	2003-518144	7001445/2004	20040367	10/205,743
		Pending	Pending	Pending		Abandoned	Nationalized	Pending	Pending	Nationalized	Pending	Pending	Pending	Pending	Pending
		Japanese Nationalization F	PARALLEL SCRAMBLER/DESCRA R MRI FR		Al	GNALS		alization	Chinese Nationalization	T	European Mationalization	Japanese Nationalization	Korean Nationalization	nonie.	COMPUTATIONAL CIRCUITS AND METHODS FOR PROCESSING MODULATED SIGNALS
	Title	Japanese N	PARALLEL SCRAMBLE MRI FR	PCT Application	COMPLITATIONA	CINETOTA NO CIRCUITS AND METHODS FOR MODULATED SI HAVING NON-CONSTANT FOR MODULATED SI HAVING NON-CONSTANT FOR MOTOTAL OPES	PCT Application	Canadian N	Chinese N	PCT Application	European	Japanese	Korean Na	Norwegian Nationalization	COMPUTATIONA CIRCUITS AND METHODS FOR PROCESSING MODULATED SIG
	tem .	ICE-008JP .	ICE-009	OE-DOGDO	וכר-ממי מ	ICE-010	ICE-010PC	ICE-010CA	ICE-010CN	ICE-011PC	ICE-010EP	ICE-010JP	ICE-010KR	ICE-010NO	ICE-010CP

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

i <u>Item</u>	Title	37	Status	Number	Published / Unpublished	and Pald	Foreign Patents	Comments
ICE-011	CHIREIX ARCHITECTU USING LOW IMPEDANCE AMPLIFIERS	3	panss	6,836,183	ssued	Yes	Yes. See Below.	pansaj
ICE-011JP	Japanese Nationalization		Pending	ТВО	твр	Yes		
ICE-011EP	European Nationalization		Pending	TBD .	Published	Yes		Response to Examination Report Due December 17, 2005.
ICE-012	MEMORY SYSTEMS AND METHOD FOR USE IN TRELLIS- BASED DECODING	rstems Dd For LLIS- ODING	Pending	10/377,860	Published	Yes		
ICE-013	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEM	TION R A SYSTEM	Pending	10/613,355	Published	Yes	·	
ICE-013CP	PREDISTORTION CIRCUIT FOR A TRANSMIT SYSTEM (CIP)	(TION PR A SYSTEM	Pending	10/641,370	Published	Yes		
ICE-014	A METHOD OF AND DEVICE FOR ANTENNAE DIVERSITY SWITCHINS	OF AND R DIVERSITY	Pending .	10/610,454	Published	Yes	PCT Application Pending.	
ICE-014PC	PCT Application	ıtion	Pending	PCT/CA2004/0009 49	Published	Yes		Nationalization Due: 12/30/05.
   ICE-015	ADAPTIVE PREDISTGRTION FOR A TRANSMIT SYSTEM	TION FOR	Allowed	10/613,372	Issue Fee Paid	Yes	. •	Allowed and ready for Issuance, Issue Fee Paid
ICE-015CP	ADAPTIVE PREDISTCRTJON FOR A TRANSMIT SYSTEM (CIP)	TION FOR IT SYSTEM	Allowable	10/641,372	Allowed	Yes		Allowed. Issue Fee Due 12/23/05,
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	-							

IceFутв Semiconductor Corp. Patent Related Information As of September 22, 2005

Comments	panss	Final (6 Month) date to respond to office action: December 17, 2005.	Notice of Appeal with Appeal Brief Due 12/30/05.		Allowed and ready for Issuance. Issue fee pald. Checking status.		Allowed. Issue fee due November 22, 2005.
Foreign Patents					PCT Application Pending.		
Fees Current and Paid	Yes	Yes	· · .	Yes	Yes	Yes	Yes
Published / B		Published	Published	Published	Issue Fee Paid	Published	Allowed
Number	6,879,209	11/099,916	10/610,497	10/627,881	10/613,856	CA/2004/000972	10/641,371
Status	panss	Pending	Pending	Pending	Allowed	Pending	Allowable
	ING E	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDANCE INVERTER FOR PARALLEL COMBINING (GIP)	INTEGRATED CIRCUIT INCORPOHATING WIRE BOND INDUCTANCE	DIGITAL BRANCH CALIBRATOR FOR AN RF TRANSMITTER	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY	cation	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)
Title	SWITCHED-MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDAN INVERTER FOR PARALLEL COMBIN	SWITCHED MODE POWER AMPLIFIER USING LUMPED ELEMENT IMPEDAN INVERTER FOR PARALLEL COMBIN (CIP)	INTEGRATED CIR INCORPORATING WIRE BOND INDUCTANCE	DIGITAL BRANCH CALIBRATOR FOR RF TRANSMITTER	ADAPTIVE PREDISTORTIO A TRANSMIT SY WITH GAIN, PH AND DELAY ADJUSTMENTS	PCT Application	ADAPTIVE PREDISTO A TRANSM WITH GAIN AND DELA ADJUSTME
Item	ICE-016	ICE-016C1	ICE-017	ICE-018	ICE-019	ICE-019PC	ICE-019CP1

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

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<u>Item</u>	Title	(A)	Status	Number	12		Foreign Patents	Comments
ICE-019CP2	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN PHASE AND DELAY ADJUSTMENTS (CIP)		Allowable	10/641,374	Allowed	Yes		Allowed. Issue fee due December 6, 2005.
ICE-019CP3	ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS (CIP)	ŭΣ	Allowabie	10/641,373	Allowed	Yes		Allowed. Issue fee due November 24, 2005.
ICE-020	STAGGERED AGC WITH DIGITALLY CONTROLLED VGA	Ţ.	Pending	10/661,945	Published	Yes .	PCT Application Filed.	
ICE-020PC	PCT Application		Pending	CA2004/001566	Published	Yes		
ICE-021	OPTIMIZED F		Pending	10/662,063	Published	Yes		
ICE-022	METHOD FOR AMPLITUDE INSENSITIVE PACKET DETECTION	ACKET	Pending	10/661,943	Published	Yes	PCT Application Filed.	
ICE-022PC	PCT Application	ro Lo	Pending	CA2004/001565	Published	Yes		
ICE-023	FREQUENCY DOMAIN EQUALIZER FOR WIRELESS COMMUNICATIONS SYSTEM	Y DOMAIN FOR TIONS	Pending	10/661,147	Published	Yes	PCT Application Filed.	
1CE-023PC	PCT Application	on	Pending	CA2004/001564	Published	Yes		
ICE-029	METHODS AND SYSTEMS FOR SIGNAL AMPLIFICATION THROUGH ENVELOPE REMOVAL AND RESTORATION	ND OR SIGNAL ION NVELOPE ND	Pending	107779,322	Not Published	, ≺es	PCT Application Filed.	
ICE-029PC	PCT Application	ion	Pending	CA2005/000153	Not Published	Yes		
849099								

IceFyre Semiconductor Corp. Patent Related Information As of September 22, 2005

				·		l 1 due 5, 2005.	
	Comments					Provisional application, Conversion due November 5, 2005.	
	CC						
!	Foreign Patents			·			
•	Fees Current Fc	Yes	Yes	Yes .	Yes	. sa,	
	Published / F Unpublished a	Not Published	Not Published	Not Published	Not Published	Not Published	
	Number	10/883,170	10/884,633	10/954,429	10/884,627	60/625,301	
•	Status	Pending : 1	Pending	Pending	Pending	Pending	
	1	SYSTEMS/AND METHODS FOR RAPID F SIGNAL DETECTION AND IDENTIFICATION	(/)	Multiple Ingut, Multiple Output Communications Systems (Continuation)	-	lated	
•	Title	SYSTEMS AND METHODS FOR RAPI SIGNAL DETECTION AND IDENTIFICATION	Multiple Inp Output Con Systems	Multiple Int Output Cor Systems (C	Power Amplifier	Improved Power Amplifier and Re Methods.	
-	Item	ICE-030	ICE-031	ICE-031C1	ICE-032	ICE-033PR	849099

# ASSIGNMENT OF PATENT RIGHTS

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- damages,
- injunctive relief and ŒŒ
- other remedies of any kind

for past, current and future infringement; and

(g) all rights to collect royalties and other payments under or on account of any of the foregoing

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Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-001	Switched-Mode Power Amplifier Integrally Performing Power Combining	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
ICE-001PC	Switched-Mode Power Amplifier Integrally Performing Power Combining	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
ICE-001JP	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2003-550250	Јарвл	Wight, James	12/3/2002
ICE-001KR	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	7008505/2004	Когеа	Wight, James	06/03/2004
ICE-001CN	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2824126.6	China	Wight, James	12/3/2001
ICE-001AU**	Switched-Mode Power Amplifier Iutegrally Performing Power Combining	Lapsed	2002351903	Australia	Wight, James	12/3/2002

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1/27/01	1/27/01	2/6/2002	7/27/01	2/6/2,002
Wight, James	Wight, Jar	Wight, Jar Wight, Jar	Wight, Jar Wight, Ja	Wight, Jar Wight, Jar Wight, Jar
U.S.A.	U.S.A.	U.S.A. U.S.A.	U.S.A. U.S.A. PCT	U.S.A. PCT Canada
60/307/889	60/307/889	60/307/889 10/068,120 CA02/01150	60/307/889 10/068,120 CA02/01150	60/307/889 10/068,120 CA02/01150 2455111
Expired	Expired	Expired Abandoned Vationalized	Expired Abandoned Vationalized Abandoned	Expired Abandoned Abandoned Abandoned Pending
Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers				
IĆE-002PR S				
	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers Reception Diversity Combiner with Selectable Inversion and Variable Gain  Selectable Inversion Abandoned Abandoned 10/068,120 U.S.A. Wight, James Wight, James	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner with Selectable Inversion and Variable Gain  Reception Diversity Combiner with Selectable Inversion and Variable Gain  Selectable Inversion and Abandoned 2455111  Canada Wight, James Wight, James	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner with Selectable Inversion and Variable Gain Reception Diversity Combiner Wight, James Wight, James Wight, James

Filing Date		7/26/2002	01/27/2004	7/26/2002	3/11/2002	10/4/2002	10/4/2002	st \$/22/2002
Inventor		Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Birkett, Alexander
Country		Јарап	Котеа	Norway	U.S.A.	PCT	Australia	U.S.A.
Number		2003-518082	7001206/2004	20040269	10/094,826	CA02/01498	2002328744	10/154,282
Status	,	Abandoned	Pending	Abandoned but revivable	Allowed	Expired	Lapsed	Pending
Title	with Selectable Inversion and Variable Gain	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Psuedo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Up/Down Conversion Circuitry for Radio Transceiver
Item		ICE-002JP	ICE-002KR	ICE-002NO	ICE-003	ICE-003PC	ICE-003AU**	ICE-004

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/23/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/02
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

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	Title	Status	Number	Country	Inventor	Filing Date
ICE-006AU**	Phasor Fragmentation Circuity and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	10/377,859	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Expired	CA04/000282	PCT	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	· Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

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Filing Date	07/31/03	7/31/2003	9/18/02	7/29/2003	7/31/2003	7/31/2003	3/23/01
Inventor	Amer, Maher	Wight, James					
Country	PCT	Australia	U.S.A.	U.S.A.	PCT	Australia	U.S.A.
Number	CA03/0113	2003249822	60/411,343	10/629,640	CA03/01132	2003249821	60/277,941
Status	Nationalized	Lapsed	Expired	Pending	Expired	Lapsed	Expired
Title	Parallel Convolutional Encoder	Parallel Convolutional Encoder	Parallel Scrambler Descrambler	Parallel Scrambler/Descrambler	Parallel Scrambler/Descrambler	Parallel Scrambler/Descrambler	Processing Engines and RF Circuitry for Multi-Carner Modulation Transceivers
Item	ICE-008PC	ICE-008AU**	ICE-009PR	ICE-009	ICE-009PC	ICE-009AU**	ICE-010PR

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Filing Date	7/30/2001	7/29/2002	7/29/2002	7/29/2002	7/29/2002	7/29/2/002	01/30/2004
Inventor	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James	Wight, James
Country	U.S.A.	PCT	Canada	China	EPO	Japan	Korea
Number	09/918,106	CA02/001174	2,455,277	20818664.8	2748528.3	2003-518144	7001445/2004
Status	Abandoned	Nationalized	Abandoned but Revivable	Pending	Pending	Abandoned	Pending
Title	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Signal Decomposition for The Control Of its Dynamic Range	Signal Decomposition for The Control Of its Dynamic Range	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Signal Decomposition for The Control Of its Dynamic Range	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Errelopes	Computational Circuits and Methods for Processing Modulated Signals Having
Item	ICE-010	ICE-010PC	ICE-010CA	ICE-010CN	ICE-010EP	ICE-010JP	ICE-010KR

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Title Status Non-Constant Envelopes	Status		Number	Country	Inventor	Filing Date
Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Ab Re	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Д	Pending	10/205,743	U.S.A.	Wight, James	7/26/2002
Chireix Architecture Using Low Iss	Iss	Issued	6836183	U.S.A.	Wight, James	10/16/2002
Chireix Architecture Using Low Per Impedance Amplifiers	P	Pending	2004-543859	Japan	Wight, James	04/15/2005
Chireix Architecture Using Low Impedance Ampliffers	Nati	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
Chireix Architecture Using Low P. Impedance Ampliffers	Ã	Pending	03769084	EPO	Wight, James	10/14/2003

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-011AU**	Chireix Architecture Using Low Impedance Amplifiers	Lapsed	2003278004	Australia	Wight, James	10/14/2003
· ICE-012	Memory Systems and Method for Use In Trellis-Based Decoding	Pending	10/377,860	U.S.A.	Amer, Maher	2/28/2003
ICE-013	Predistortion Circuit for a Transmit System	Pending	10/613,355	U.S.A.	Saed, Aryan	7/3/2003
ICE-013CP	Predistortion Circuit for a Transmit System (CIP)	Pending	10/641,370	U.S.A.	Saed, Aryan	8/13/2003
ICE-014	A Method Of and Device for Antennae Diversity Switching	Pending	10/610,454	U.S.A.	Saed, Aryan	6/30/2003
ICE-014PC	A Method Of and Device for Receive Antennae Diversity Switching	Pending	CA04/000949	PCT	Saed, Aryan	6/23/04
ICE-015	Adaptive Predistortion for a Transmit System	Allowed	10/613,372	U.S.A.	Saed, Aryan	7/3/2003
ICE-015CP	Adaptive Predistortion for a Transmit System (CIP)	Allowed	10/641,372	U.S.A.	Saed, Aryan	8/13/2003

Item	Title	Status	Number	Country	Inventor	Filing Date
ICE-016	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining	Issued	6,879,209	U.S.A.	Grundingh, Johan	7/8/2003
ICE-016CP	Switched-Mode Power. Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP)	Pending	11/099,916	U.S.A.	Grundingh, Johan	4/6/2005
ICE-017	Integrated Circuit Incorporating Wire Bond Inductance	Pending	10/610,497	U.S.A.	Wight, James	6/30/2003
ICE-018	Digital Branch Calibrator for An RF Transmitter	Pending	10/627,881	U.S.A.	Saed, Aryan	7/25/2003
ICE-019	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Allowed	10/613,856	U.S.A.	Saed, Aryan	7/3/2003
ICE-019PC	Adaptive Predistortion for a Transmit System with Gain,	Pending	CA04/000972	PCT	Saed, Aryan	6/30/2004

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Filing Date		8/13/2003	8/13/2003	8/13/2003.	9/12/2003	8/26/2004	9/12/2003	9/12/2003
Inventor		Saed, Aryan	Saed, Aryan	Saed, Aryan	Birkett, Neil	Birkett, Neil	Amer, Maher	Birkett, Neil
Country		U.S.A.	U.S.A.	U.S.A.	U.S.A.	PCT	U.S.A.	U.S.A.
Number		10/641,371	10/641,374	10/641,373	10/661,945	CA04/001566	10/662,063	10/661,943
Status	,	Allowed	Allowed	Allowed	Pending	Pending	Pending	Pending
Title	Phase and Delay Adjustments	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Staggered AGC with Digitally Controlled VGA	Staggered AGC with Digitally Controlled VGA	Optimized FFT/IFFT Module	Method for Amplitude Insensitive Packet Detection
Item		ICE-019CP1	ICE-019CP2	ICE-019CP3	ICE-020	ICE-020PC	ICE-021	ICE-022

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Filing Date	8/26/2004	9/12/2003	8/26/04	2/13/2004	2/7/2005	07/01/2004	07/02/2004
Inventor	Birkett, Neil	Saed, Aryan	Saed, Aryan	Wight, James	Wight, James	Moher, Michael L.	Wight, James
Country	PCT	U.S.A.	PCT	U.S.A.	PCI	U.S.A.	U.S.A.
Number	CA04/001565	10/661,147	CA04/001564	10/779,322	CA05/000153	10/883,170	10/884,633
Status	Pending	Pending	Pending	Pending	Pending	Pending	Pending
Title	Method for Amplitude Insensitive Packet Detection	Frequency Domain Equalizer for Wireless Communications System	Frequency Domain Equalizer for Wireless Communications System	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Systems and Methods for Rapid Signal Detection and Identification	Multiple Input, Multiple Output Communications Systems
Item	ICE-022PC	ICE-023	ICE-023PC	ICE-029	ICE-029PC	ICE-030	ICE-031

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Title	Status		Number	Country	Inventor	Filing Date
Multiple Input, Multiple Output Communications Systems (CIP)	ا ہو ج	Pending	10/954,429	U.S.A.	Wight, James	09/30/2004
Power Amplifier Per	Pet	Pending	10/884,627	U.S.A.	Parker, Kevin	7/02/2004
Improved Power Amplifier and ExRelated Methods.	白	Expired	60/325,301	U.S.A.	Grundlingh, Johan	11/05/2004
Power Amplifier Pe	Pd	Pending	Not yet assigned	U.S.A.	Grundlingh, Johan	11/07/2005

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IN WITNESS WHEREOF this Assignment of Fatont Rights is executed at

ASSIGNOR

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By: /(User (1150 ==

Name: Michael F. Schi

Title: Director

(Signature MUST be notarized)

STATE OF MUSCACHUSEHS

Notary Public in and for said State, before me,

same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the personally known to me (or proved to me on the basis of satisfactory the person acted, executed the instrument. personally appeared ////

WITNESS my hand and official seal.

Signature Multin

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